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Atty Dkt: 1247/A38

Date: February 14, 2000

Invention: GRAPHICS PROCESSOR WITH TEXTURE MEMORY ALLOCATION SYSTEM

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION DISCLOSURE STATEMENT

Exam.	Ref.	Document	Issue		
<u>Init.</u>	<u>No.</u>	<u>Number</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>
D5C	AA	4,434,437	Feb. 28, 1984	Strolle et al.	358/140
D7C	AB	4,615,013	Sep. 30, 1986	Yan et al.	364/521
D7C	AC	4,646,232	Feb. 24, 1987	Chang et al.	364/200
D7C	AD	4,908,780	March 13, 1990	Priem et al.	364/521
D7C	AE	4,918,626	April 17, 1990	Watkins et al.	364/522
D7C	AF	4,991,122	Feb. 5, 1991	Sanders	364/521
D7C	AG	5,107,415	April 21, 1992	Sato et al.	395/800
D7C	AH	5,123,085	June 16, 1992	Wells et al.	395/121

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07c	AI	5,239,654	Aug. 24, 1993	Ing-Simmons et al.	395/800
07c	AJ	5,287,438	Feb. 15, 1994	Kelleher	395/132
07c	AK	5,293,480	March 8, 1994	Miller et al.	395/163
07c	AL	5,313,551	May 17, 1994	Labrousse et al.	395/425
07c	AM	5,363,475	Nov. 8, 1994	Baker et al.	395/122
07c	AN	5,371,840	Dec. 6, 1994	Fischer et al.	395/133
07c	AO	5,394,524	Feb. 28, 1995	DiNicola et al.	395/163
07c	AP	5,398,328	March 14, 1995	Weber et al.	395/500
07c	AQ	5,446,479	Aug. 29, 1995	Thompson et al.	345/139
07c	AR	5,485,559	Jan. 16, 1996	Sakaibara et al.	395/133
07c	AS	5,511,165	April 23, 1996	Brady et al.	395/200.01
07c	AT	5,519,823	May 21, 1996	Barkans	395/143
07c	AU	5,544,294	Aug. 6, 1996	Cho et al.	395/141
07c	AV	5,555,359	Sep. 10, 1996	Choi et al.	395/141
07c	AW	5,557,734	Sep. 17, 1996	Wilson	395/162
07c	AX	5,561,749	Oct. 1, 1996	Schroeder	395/120
07c	AY	5,572,713	Nov. 5, 1996	Weber et al.	395/500
07c	AZ	5,631,693	May 20, 1997	Wunderlich et al.	348/7
07c	BA	5,664,114	Sep. 2, 1997	Krech, Jr. et al.	395/200.64
07c	BB	5,666,520	Sep. 9, 1997	Fujita et al.	345/513
07c	BC	5,684,939	Nov. 4, 1997	Foran et al.	395/131
07c	BD	5,701,365	Dec. 23, 1997	Harrington et al.	382/212
07c	BE	5,706,481	Jan. 6, 1998	Hannah et al.	395/519
07c	BF	5,721,812	Feb. 24, 1998	Mochizuki	395/110
07c	BG	5,737,455	April 7, 1998	Harrington et al.	382/284
07c	BH	5,757,375	May 26, 1998	Kawase	345/429
07c	BI	5,757,385	May 26, 1998	Narayanaswami et al.	345/505
07c	BJ	5,764,237	June 9, 1998	Kaneko	345/430
07c	BK	5,821,950	Oct. 13, 1998	Rentschler et al.	345/505
07c	BL	5,841,444	Nov. 24, 1998	Mun et al.	345/506
07c	BM	5,870,567	Feb. 9, 1999	Hausauer et al.	395/281
07c	BN	5,883,641	March 16, 1999	Krech, Jr. et al.	345/505
07c	BO	5,914,711	June 22, 1999	Mangerson et al.	345/203

Examiner: Daniel J Chang

Date Considered: 3/20/01

NOTE FOR EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance AND not considered. Include copy of this form with next communication to applicant.

Foreign Patent Documents

<u>Exam. Ref.</u> <u>Init. No.</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Country</u>
BP	WO 86/07646	Dec. 31, 1986	World
BQ	0 311 798 A2	April 19, 1989	Europe
BR	0 397 180 A2	Nov. 14, 1990	Europe
BS	0 438 194 A2	July 24, 1991	Europe
BT	0 448 286 A2	Sept. 25, 1991	Europe
BU	0 463 700 A2	Jan. 2, 1992	Europe
BV	WO 92/00570	Jan. 9, 1992	World
BW	0 566 229 A2	Oct. 20, 1993	Europe
BX	WO 93/06553	April 1, 1993	World
BY	0 627 682 A1	Dec. 7, 1994	Europe
BZ	0 631 252 A2	Dec. 28, 1994	Europe
CA	0 693 737 A2	Jan. 24, 1996	Europe
CB	0 734 008 A1	Sept. 25, 1996	Europe
CC	0 735 463 A2	Oct. 2, 1996	Europe
CD	0 810 553 A2	Dec. 3, 1997	Europe
CE	WO 97/21192	June 12, 1997	World
CF	0 840 279 A2	May 6, 1998	Europe
CG	0 825 550 A2	Feb. 25, 1998	Europe
CH	0 817 009 A2	Jan. 7, 1998	Europe

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Other Documents

Exam. Ref.

Init. No. Name

- CI "A Fine Grained Data Flow Machine and Its Concurrent Execution Mechanism," Iwashita et al., C&C Information Technology Research Labs, April 1989, pp. 63-72.
- CJ "A Dataflow Image Processing System TIP-4," Fujita et al., C&C Information Technology Research Labs, NEC Corporation, September 1989, pp. 735-741.
- CK "Processing the New World of Interactive Media," Rathnam, The Trimedia VLIW CPU Architecture, March 1998, pp. 108-117.
- CL "Effective Cache Mechanism for Texture Mapping," IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, pp. 213-217.
- CM "Advanced Raster Graphics Architecture," XP-002118066, pp.890-893.
- CN "Data-Format Conversion: Intel/Non-Intel," Vol. 33, No. 1A, June 1990, IBM Technical Disclosure Bulletin, pp. 420-427.
- CO "Address Munging Support in a Memory Controller/PCI Host Bridge for the PowerPC 603 CPU Operating in 32-Bit Data Mode," IBM Technical Disclosure Bulletin, Vol. 38, No. 09, September 1995, pp. 237-240.
- CP "One Frame Ahead: Frame Buffer Management for Animation and Real-Time Graphics," XP-000749898, Auel et al., Tektronix Inc., pp. 43-50.
- CQ "Efficient Alias-Free Rendering Using Bit-Masks and Look-Up Tables," Abram et al., The University of North Carolina at Chapel Hill, XP-002115680, July 1985, pp. 53-59
- CR "A New Simple and Efficient Antialiasing with Subpixel Masks," Schilling et al., Computer Graphics, Vol. 25, No. 4, July 1991, pp. 133-141.

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- CS "A Multiprocessor System Utilizing Enhanced DSP's for Image Progressing," Ueda et al., XP 2028756, pp. 611-619.
- CT "The Reyes Image Rendering Architecture," Cook et al., Computer Graphics, Vol. 21, No. 4, July 1987, pp. 95-102.
- CU "The Accumulation Buffer: Hardware Support for High-Quality Rendering," Haeberli et al., Computer Graphics, Vol. 24, No. 4, August 1990, pp. 309-318.
- CV "Advanced Animation and Rendering Techniques," Watt et al., ACM Press, New York, New York, pp.127-137.
- CW "The A-Buffer, an Antialiased Hidden Surface Method, Carpenter, Loren, Computer Graphics, Vol. 18, No. 3, July 1984, pp. 13-18.

Examiner:_____

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Section 6. Copies of Listed Information Items Accompanying This Statement

NOTE: 37 C.F.R. 1.98(a)(2) requires that any information disclosure statement filed under § 1.97 shall include: "A legible copy of: (i) Each U.S. and foreign patent; (ii) Each publication or that portion which caused it to be listed; and (iii) All other information or that portion which caused it to be listed, except that no copy of a U.S. patent application need be included . . ."

NOTE: The wording in § 1.98(a)(2)(iii) makes it clear that the requirement to submit a copy of each item of information listed in an information disclosure statement does not apply to the citation of a U.S. patent application. Notice of January 9, 1992, 1135 O.G. 13-25, at 14.

Legible copies of all items listed in Form PTO-1449 (PTO/SB/08A and 08B) accompany this information statement.

(complete the following, if applicable)

☐ Exception(s) to above:

- ☐ Items in prior application, from which an earlier filing date is claimed for this application, as identified in Section 4.
- ☐ Cumulative patents or publications identified in Section 5.

(Information Disclosure Statement—Section 6. Copies of Listed Information Items Accompanying This Statement [6-1]—page 9 of 10)

Section 10. Identification of Person(s) Making THIS INFORMATION DISCLOSURE STATEMENT

The person making this statement is

(check each applicable item)

- (a) ☐ the inventor(s) who signs below

SIGNATURE OF INVENTOR

(type name of inventor who is signing)

- (b) ☐ an individual associated with the filing and prosecution of this application (37 C.F.R. 1.56(c))

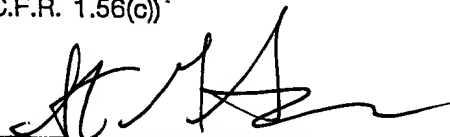
SIGNATURE OF INVENTOR

(type name of inventor who is signing)

- (c) ☒ the attorney who signs below on the basis of the information:

(check each applicable item)

- ☐ supplied by the inventor(s).
☐ supplied by an individual associated with the filing and prosecution of this application. (37 C.F.R. 1.56(c))
☒ In the attorney's file.



SIGNATURE OF PRACTITIONER

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(Information Disclosure Statement—Section 10. Identification of Person(s) Making This Information Disclosure Statement [6-1]—page 10 of 10)